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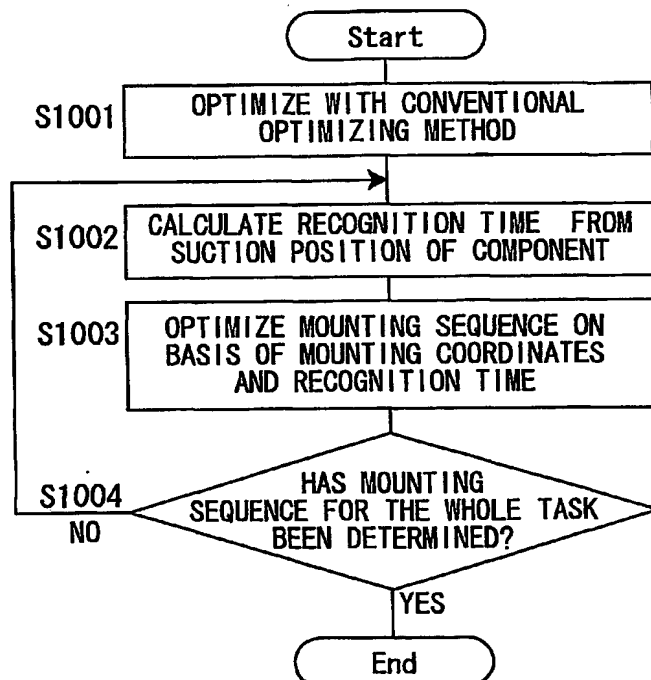
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[Continued on next page]

(54) Title: COMPONENT MOUNTING SEQUENCE OPTIMIZING METHOD, COMPONENT MOUNTING DEVICE, PRO-
GRAM FOR EXECUTING COMPONENT MOUNTING SEQUENCE OPTIMIZING METHOD, AND RECORDING MEDIUM
IN WHICH THE PROGRAM IS RECORDED



(57) Abstract: The present invention provides a component mounting sequence optimizing method, a component mounting device, a program for executing the component mounting sequence optimizing method, and recording medium in which the program is recorded, in the component mounting device with a component holding head capable of holding a plurality of components and having a component recognition camera. The invention includes a component mounting sequence optimizing device (300) and a mounting sequence for components is determined on basis of a result of comparison between conveyance times for the components and recognition times required for recognitions of the components. Thus cycle time for mounting can be reduced, in comparison with conventional arts, in mounting of components with a component holding head having a plurality of component holding members and having a component recognition camera.



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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 02/13590 A (MATSUSHITA ELECTRIC IND CO LTD) 14 February 2002 (2002-02-14) page 2, line 24 - page 3, line 4; figure 1 -----	1-12
A	WO 00/67542 A (SIEMENS AKTIENGESSELLSCHAFT) 9 November 2000 (2000-11-09) abstract -----	1,4,7,10

☐ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

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